

NEXTFLEX[®]



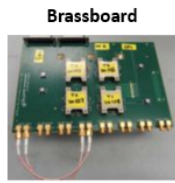
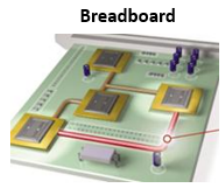
ADDITIVE ELECTRONICS FOR NOVEL WEARABLE DEVICES

DR. PATRICIA BECK, DR. ROBERT MALAKHOV, JEFF BERGMAN

Outline

- I. NextFlex Overview
- II. Flexible Hybrid Electronics
- III. Rigid to Flex Conversion
 - a. Phase I: Thin Die Attach
 - b. Phase II: Design and Manufacturing an FHE Prototype
 - c. Phase III: Transition to Volume Manufacturing
- IV. Process Improvements and Wearable Applications
- V. Summary

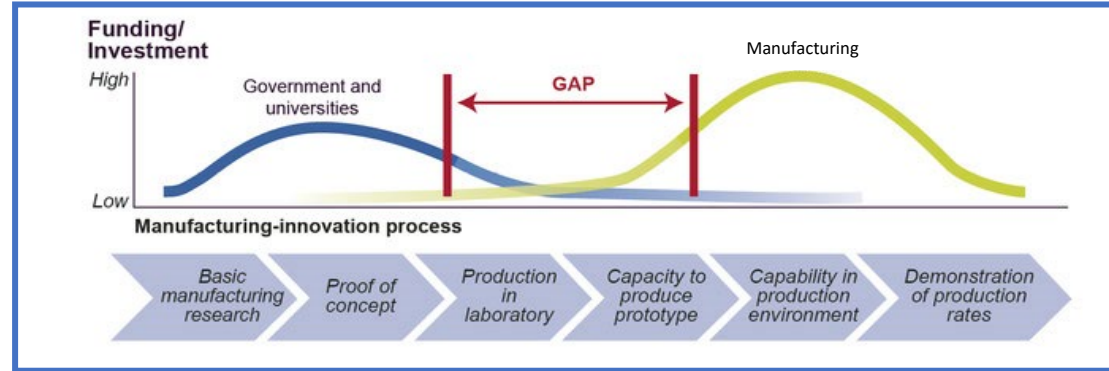
Where Nextflex (a Public/Private Partnership) Fits



- Focus Examples:**
- Technology and Industrial Base
 - Materials
 - Cost and Funding

- Focus Examples:**
- Design
 - Process Capability and Control
 - Quality Management

- Focus Examples:**
- Manufacturing Workforce
 - Facilities
 - Manufacturing Management

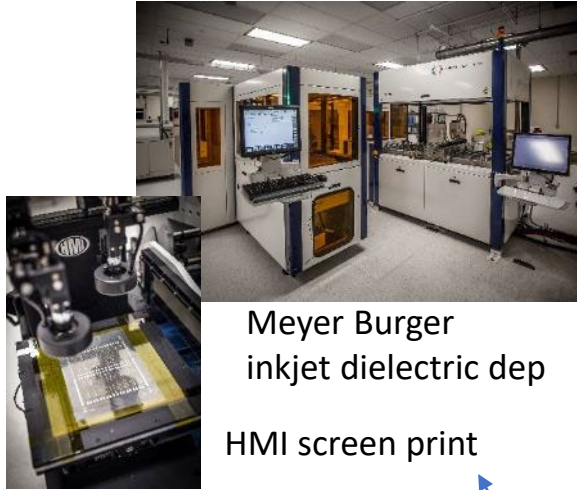


Source: GAO adapted from Executive Office of the President.

Pre Material Solution Analysis			Material Solution Analysis	Technology Maturation and Risk Reduction		Engineering & Manufacturing Development		Production & Deployment	
MRL 1	MRL 2	MRL 3	MRL 4	MRL 5	MRL 6	MRL 7	MRL 8	MRL 9	MRL 10
Basic Mfg Implications Identified	Mfg Concepts Identified	Mfg Proof of Concept Developed	Manufacturing Processes In Lab Env't	Components In Production Relevant Env't	System or Subsystem In Production Relevant Env't	System or Subsystem In Production Representative Environment	Pilot Line Demonstrated Ready for LRIP	LRIP Demonstrated Ready for FRP	FRP Demo'd Lean Production Practices in Place
TRL 1	TRL 2	TRL 3	TRL 4	TRL 5	TRL 6	TRL 7	TRL 8	TRL 9	
Basic Principles Observed	Concept Formulation	Proof of Concept	Breadboard in Lab	Breadboard in Representative Environment	Prototype in Representative Environment	Prototype in Operational Environment	System Qual	Mission Proven	



Printing and Processing, Thin Die Integration & Testing Services for Commercial and Agency Projects



Meyer Burger
inkjet dielectric dep
HMI screen print



Komori gravure print



Besi Datacon (placement
and dispense)



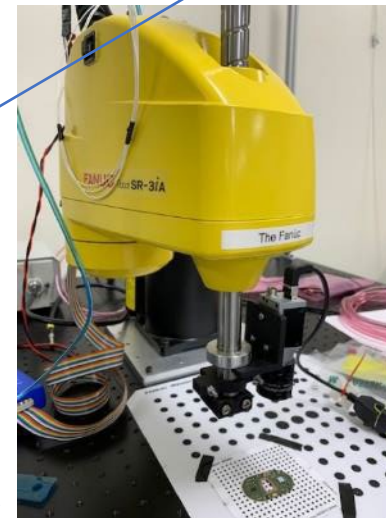
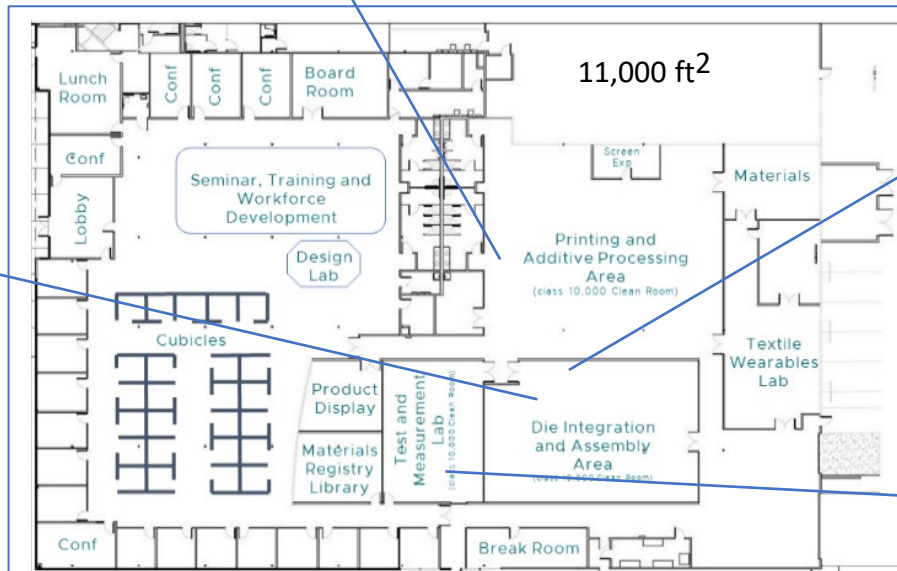
Universal Instruments
Pick and place (+ wafer
feeder for die attach)



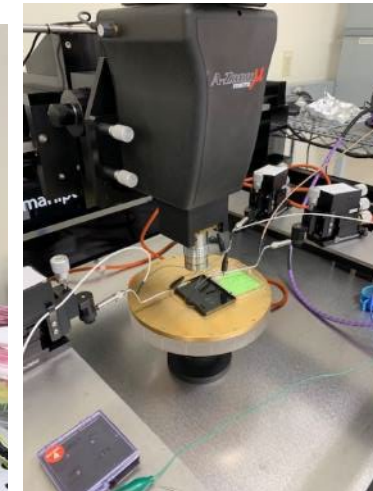
Nordson Asymtek (dep and
epoxy encapsulation for LEDs)



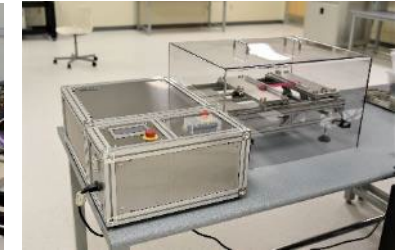
ATP Bonder



Fanuc Robot (test, component
placement, epoxy dep)



Micromanipulator
Probe station



Yuasa 1

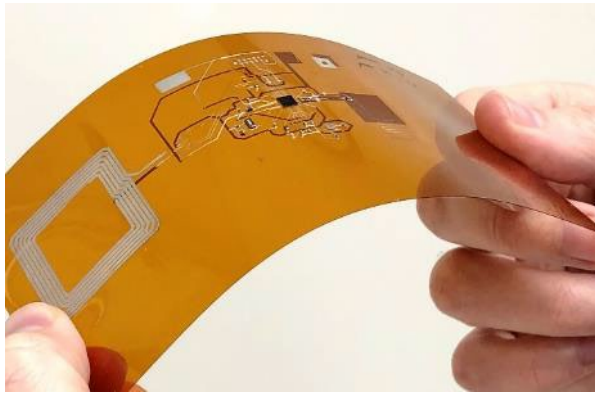


Yuasa 2

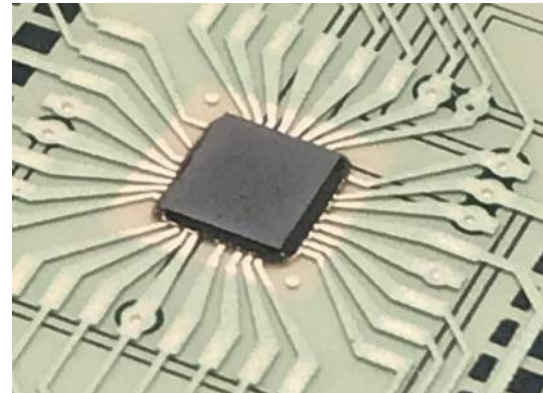
Unique Advantages of Flexible Hybrid Electronics



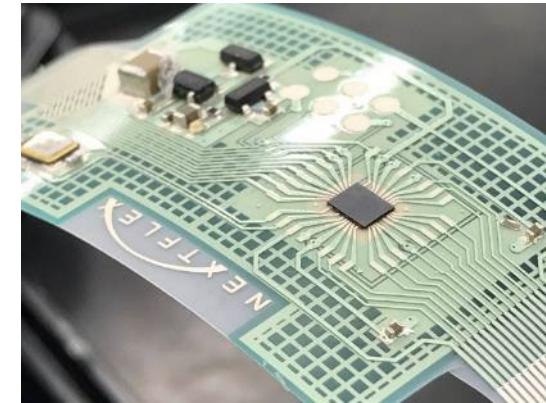
FHE combines the flexibility and low cost of printed plastic film substrates with the performance of semiconductor devices to create a new category of electronics (form factor, function, improved size, weight, power).



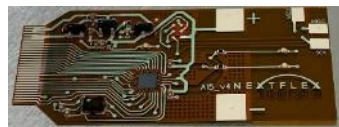
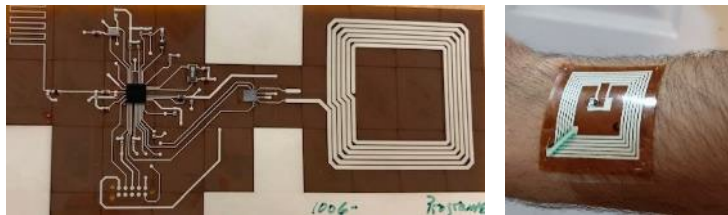
Thin, Lightweight, Flexible, Conformable



Semiconductors

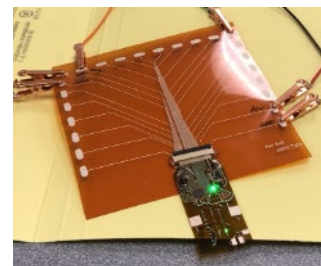


BLE-Enabled Arduino Mini Clone



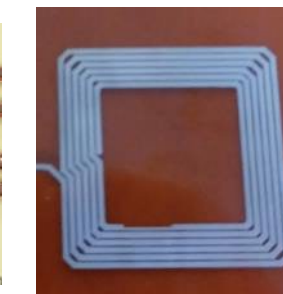
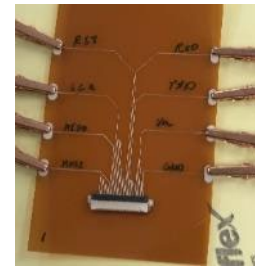
High-Mix Low-Volume Digital Manufacturing

Day 1 Design

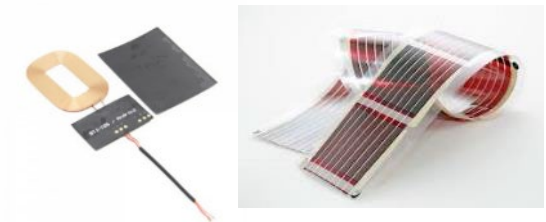


Rapid Prototyping and Design Customization

Day 2 Design



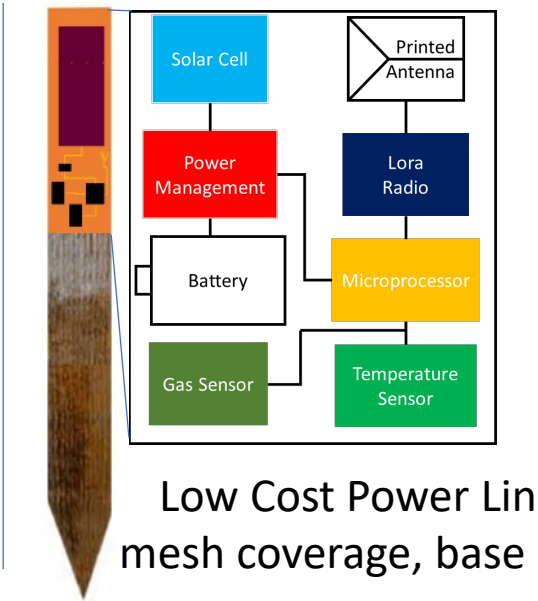
Wireless Communications for Data Transfer



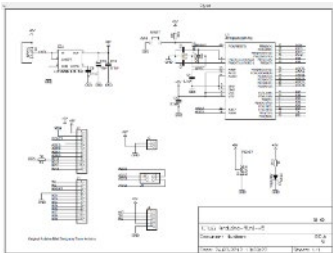
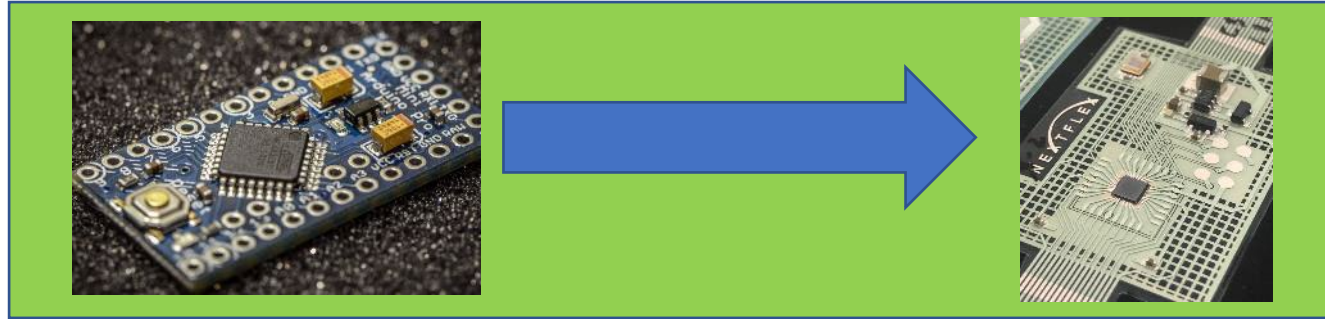
Power Harvesting Options (Qi, solar, etc.)



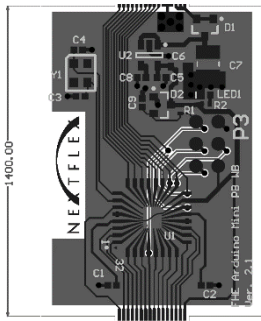
Structural Health Monitoring, Asset Management & System Level Integration



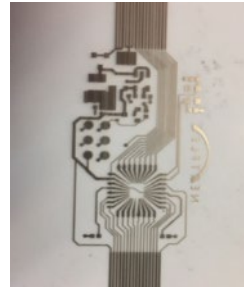
Rigid to Flex Conversion of an Arduino® Microcontroller



Single layer interconnect



System Layout



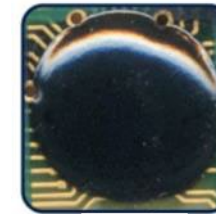
Ink Jet
Aerosol Jet
Screen Print



Flip Chip Attach



Pick & Place



Apply Bead of UV
Epoxy then
Insert-Molding or
Adhesive Glob Top



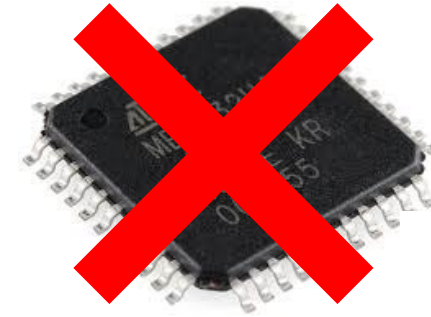
Electrical
Physical
Environmental

Greater than 50% reduction in the number of process steps (7 FHE) vs. rigid PCB manufacturing (19)



Commercial Off-The-Shelf IC's Are Not Made for FHE

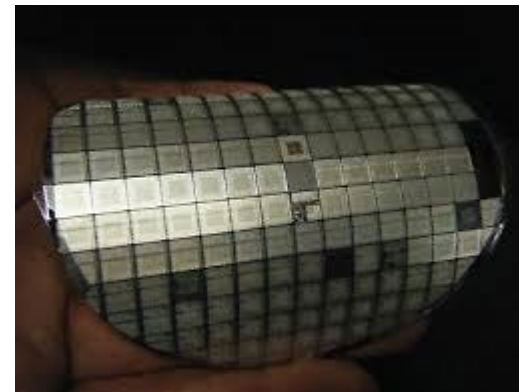
Packaged IC's are large and do not bend or flex.



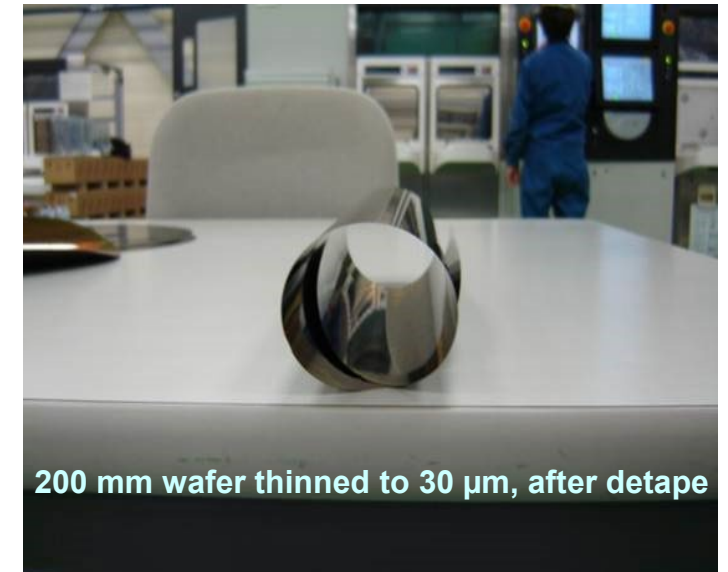
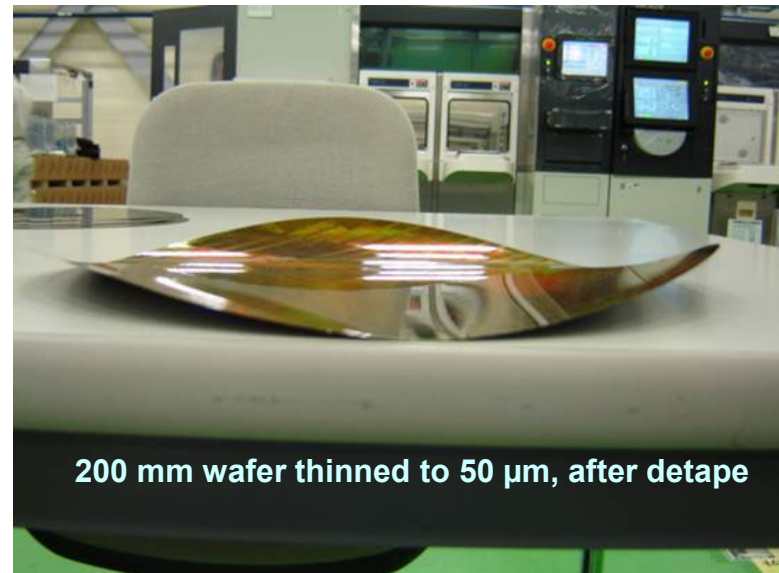
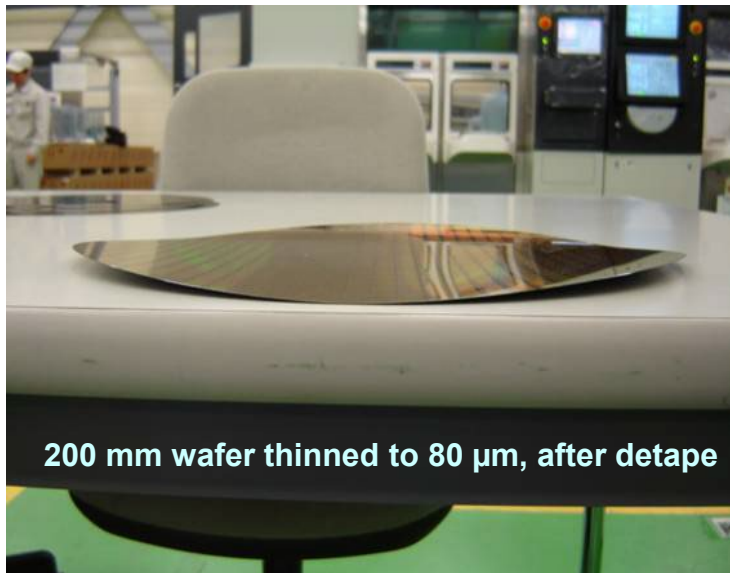
Advanced packaging wafer-level based technologies like Wafer Large Scale Chip Package (WLCSP) have large Ball Grid Arrays (BGA) which prevent thinning to our target of 50 microns or less. Solder balls of 350-micron diameter on a 50-micron die will not help reliability.



Our goal is to use wafers – thinned and diced – for direct integration into FHE devices.



Challenge: Thinning and Dicing To <50 Microns

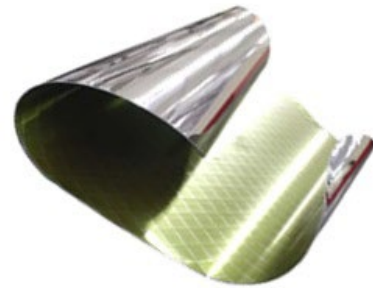


After thinning, there is the further challenge of pick and place of these ultra thin die
Requires ejection tool using multiple ejection pins per die and small distances between tape and pickup tool

Creating a Process Platform for Integration

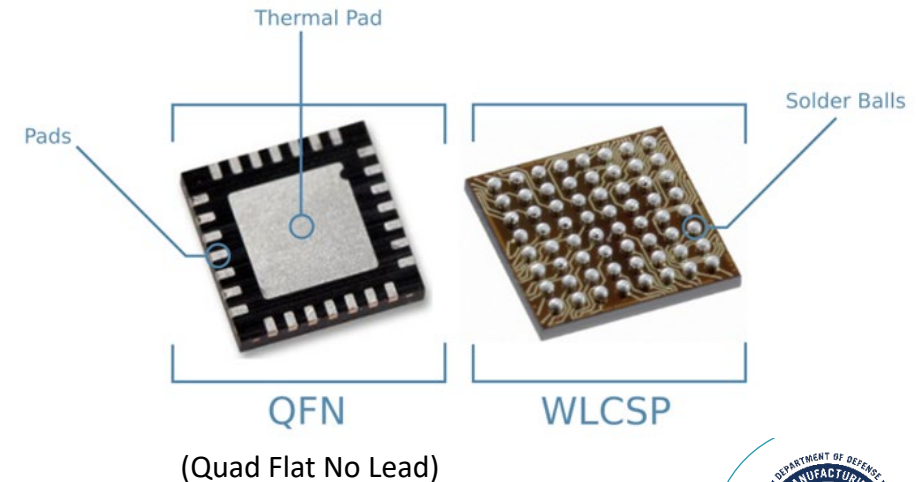
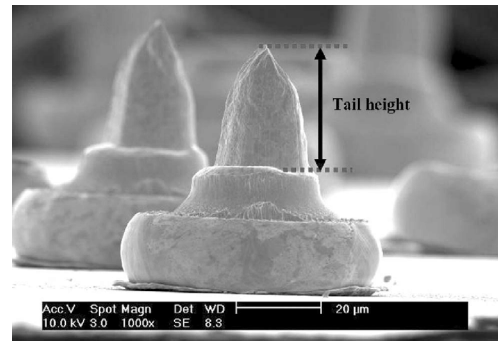
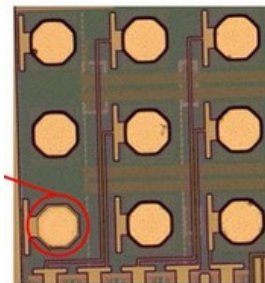
Instead of attempting to narrow down the selection of devices to integrate into an FHE system, we created a process flow where any type of device available in wafer form can be integrated:

Thinning
Dicing/Singulation
Attach

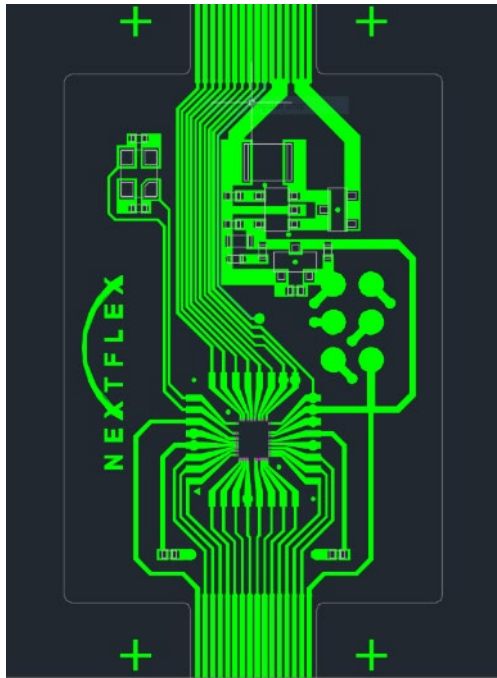


Bare die from Wafer Level Chip Scale Package (WLCSP) – bumped or unbumped

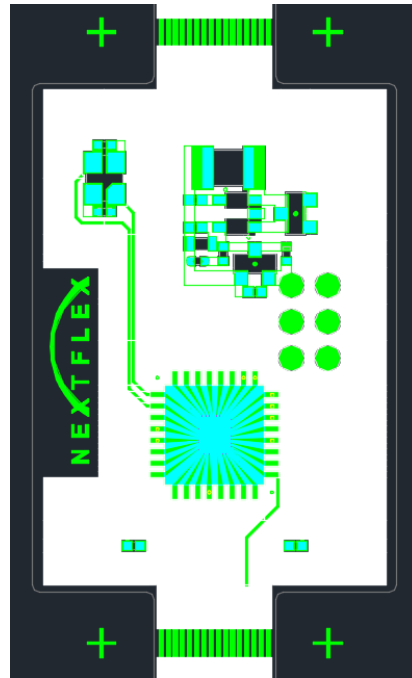
Die preparation for integration on FHE board
ENIG (Electroless Nickel Immersion Gold) bumping of contact pads
Au stud bumping of contact pads



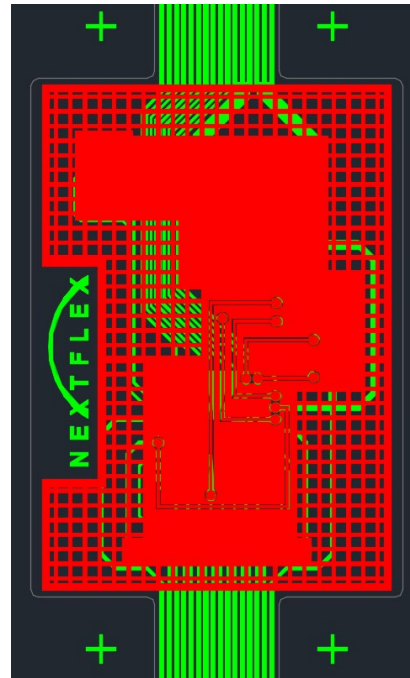
Phase II - FHE Design and Prototype



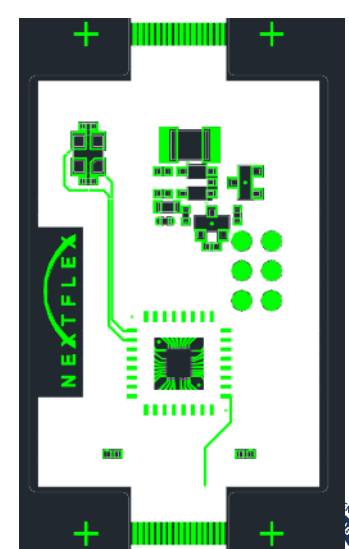
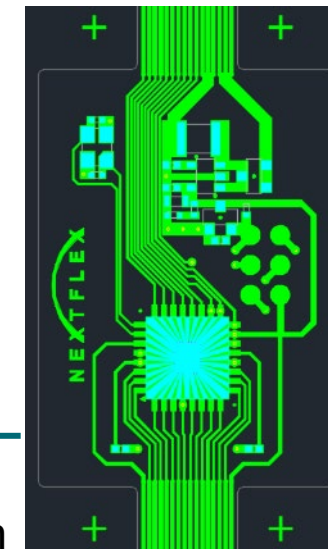
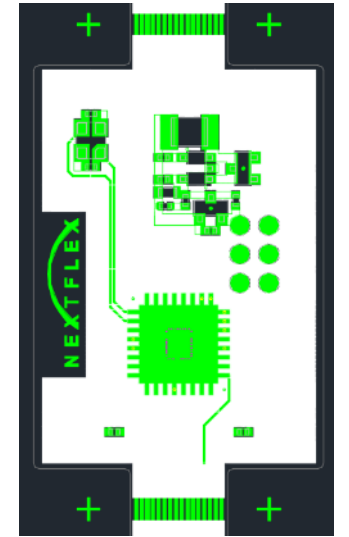
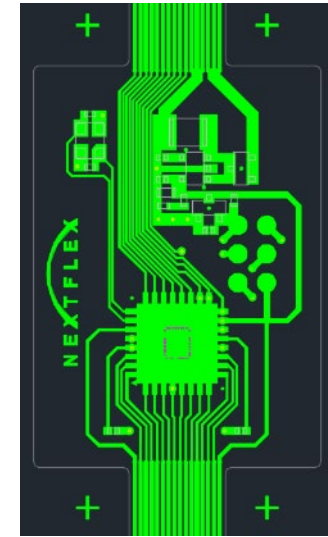
Front Side Metal



Dielectric Front



Backside Metal

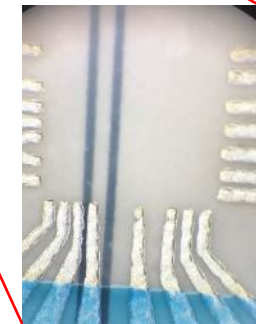
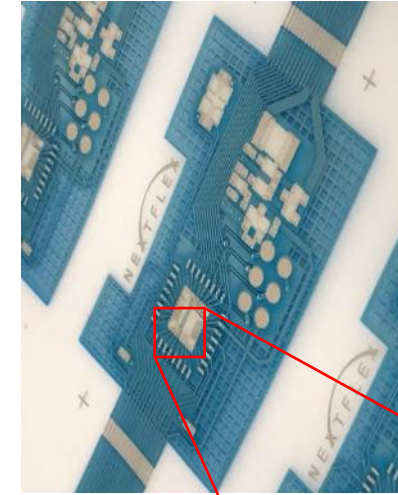
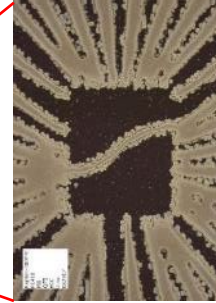
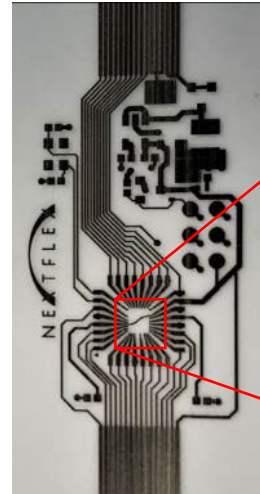
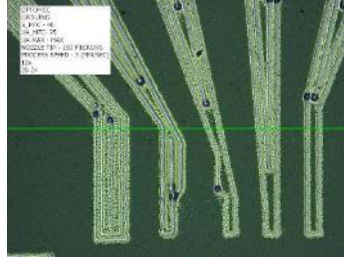
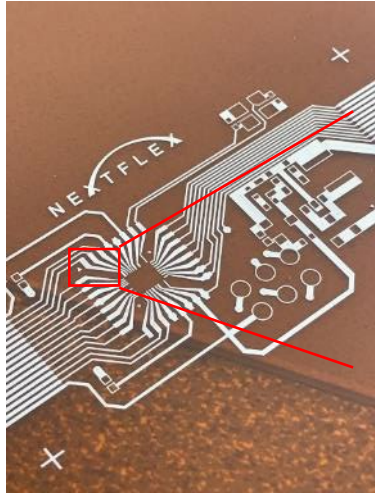


Laser Ablation

Print Files

- Design of the boards for two layers per side, one metal and one dielectric
- Laser cutting of vias in substrate prior to printing
- Laser ablation after printing for tuning of fine features and details
- Design was created to be manufactured on an 8-inch x 12-inch sheet

Down Selection of Printing Process



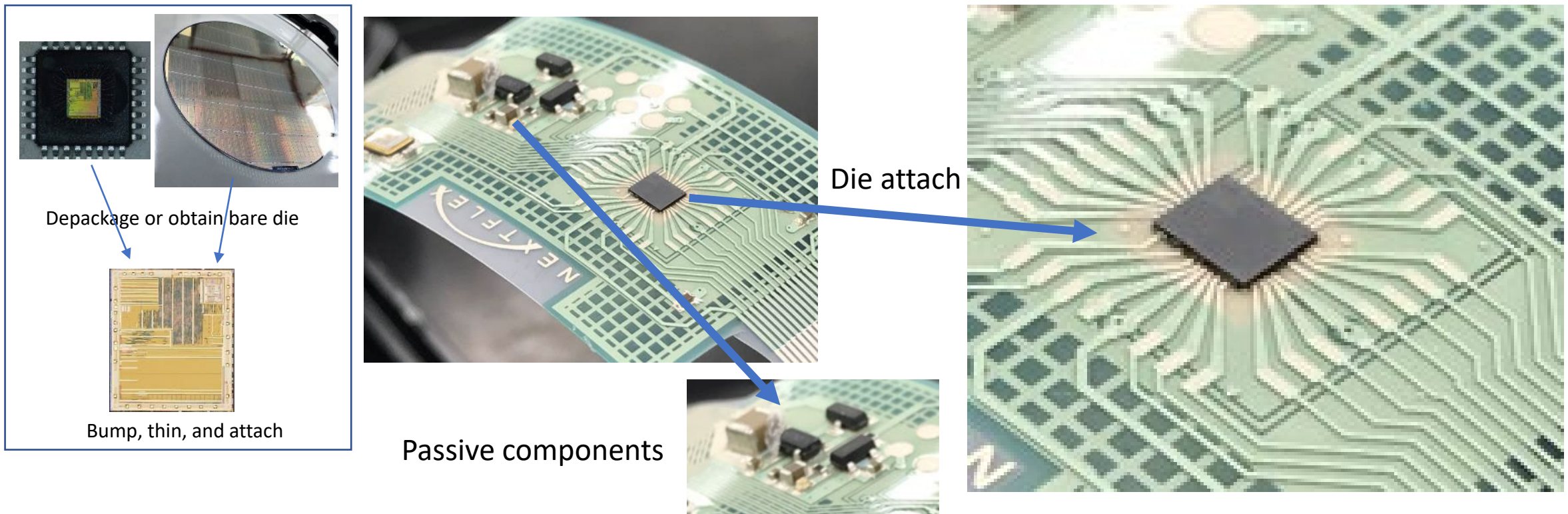
Aerosol Jet

Inkjet

- Multiple printing processes were evaluated for the print
 - primary candidates: Aerosol Jet, Ink Jet and Screen Printing
- Screen printing was chosen as preferred fabrication method
 - Fast print time
 - Good ink thickness
 - Acceptable resolution

Screen Print front and back of FHE Arduino-compatible microcontroller

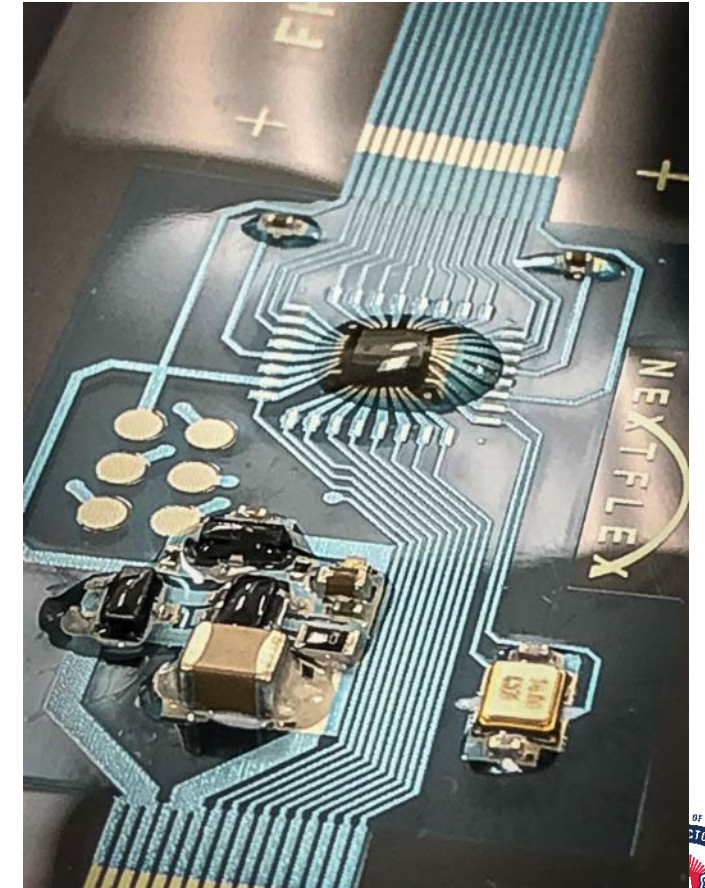
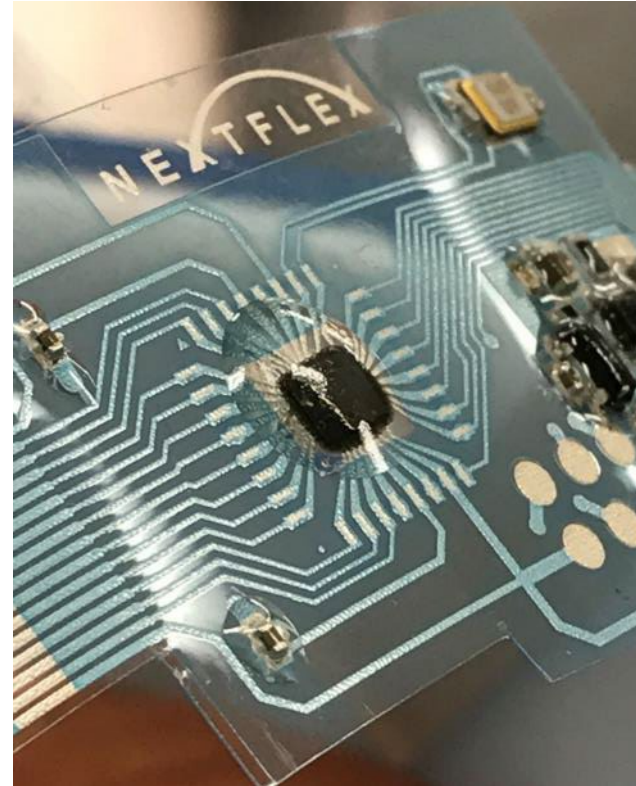
Flexible Arduino®-Compatible Microcontroller Before Encapsulation



- To develop the flexible-design bare Microchip ATmega328 (microcontroller used in all Arduino minis) die were obtained through package removal of commercially sourced IC's before being able to obtain bare die
- Both passive components and bare die were attached to the printed traces on the substrate to assemble the full device
- Once the assembly was complete the device build was finalized via encapsulation of the IC

Encapsulation

- IC's on the device were encapsulated using a syringe-deposited silicone
- Each IC was separately encapsulated to maximize the flex of the IC
- Once the silicone was deposited it was cured at room temperature
- A dielectric was screen-printed
- Later encapsulation would be by pouring and insert-molding

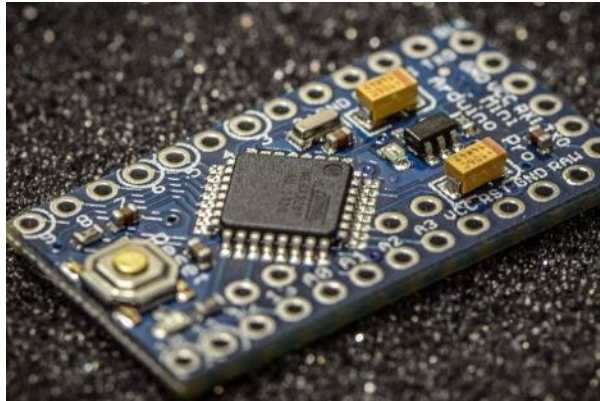


Transition to Manufacturing: Concept

Goal: Develop a mass-produced flexible copy of the Arduino®-compatible Mini Microcontroller board to:

- Validate ability to mass produce printed hybrid electronics
- Minimize the cost of the device
- Create and validate an 8-bit microcontroller platform for potential future applications

Rigid Arduino Mini Board



Flexible Prototype



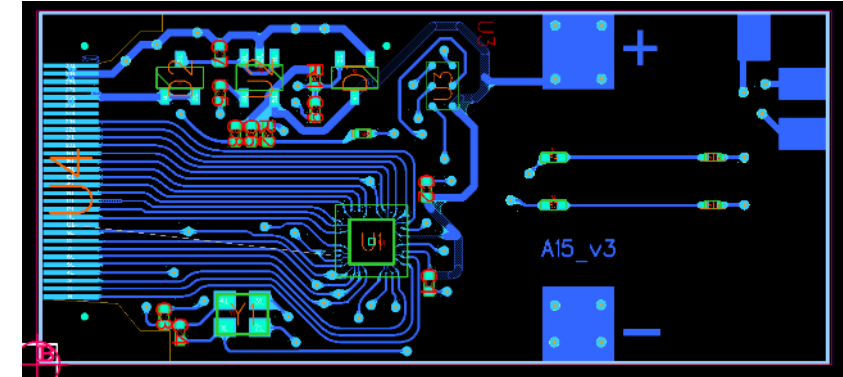
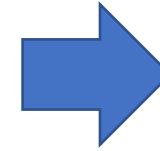
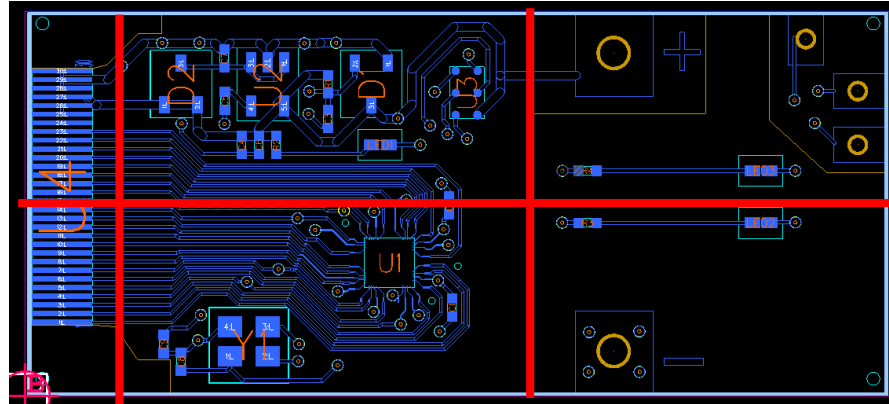
Mass Produced



Case Study: Design Process & Application of Design Rules

Schematic and layout were performed in Mentor Graphics design software

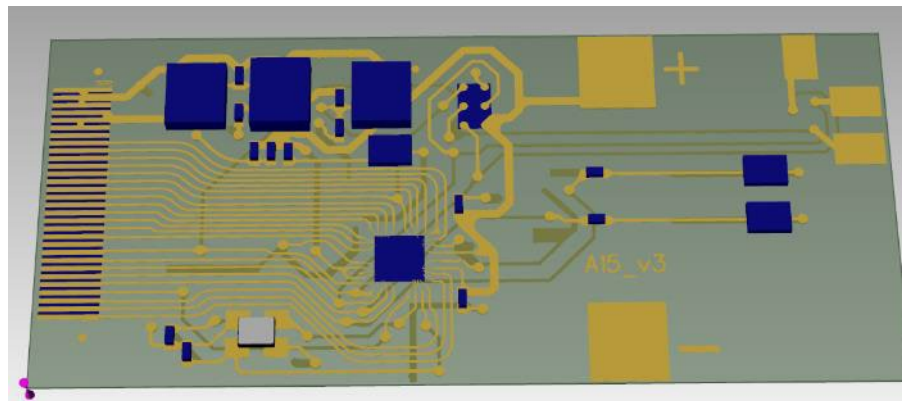
Desired bend locations were determined and components placed accordingly



Graphical files for printer were generated in Fab3000



3D Models were generated

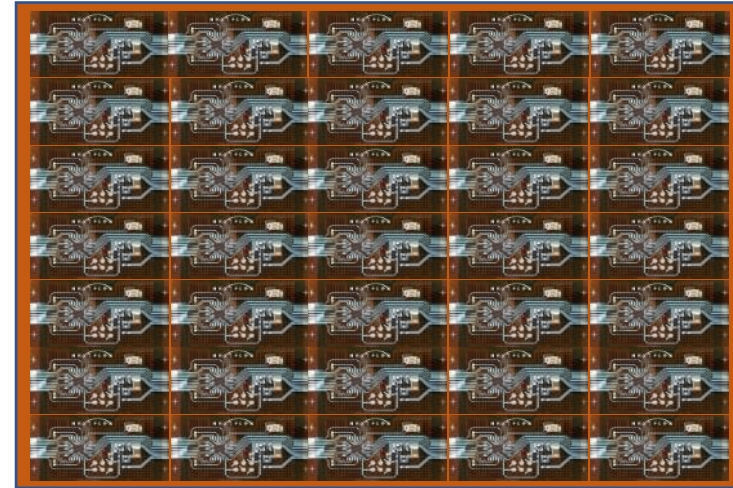
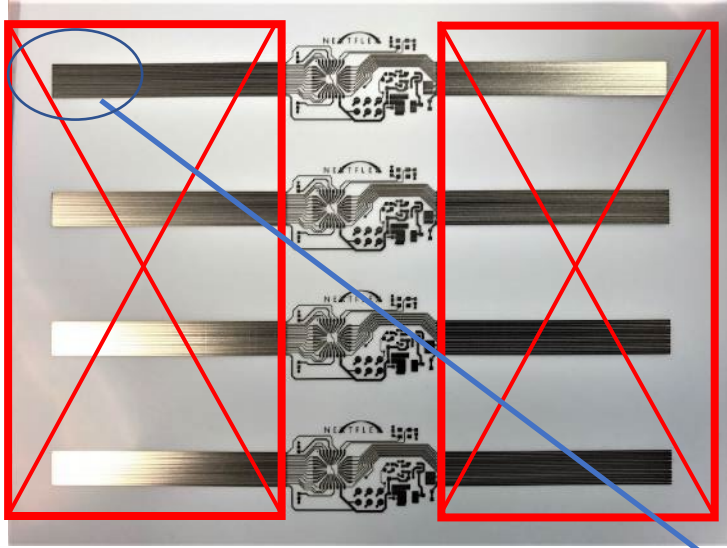


Applied design rules include:

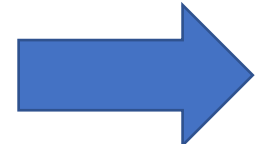
- Minimum trace l/s of 150 μ m/150 μ m
- Minimum via (80 μ m)
- Minimum pad size (250 μ m)
- Mechanical layout based on desired bend locations

Case Study: Redesign for Volume Fabrication

Removal of extraneous ears of "wristband" circuitry



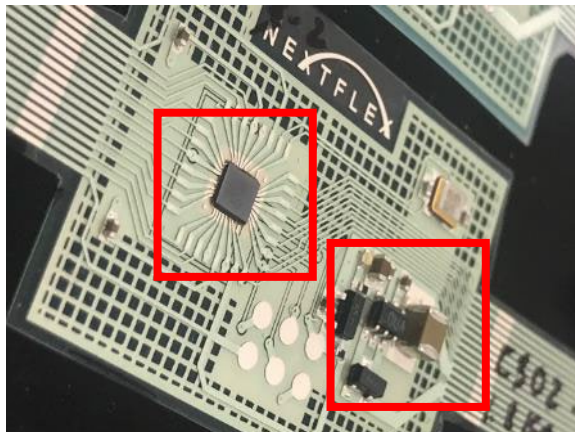
High device density per sheet to minimize waste



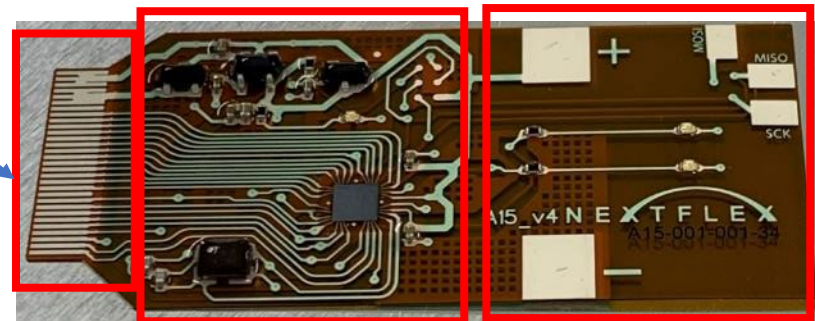
Move all ZIF pins to one side to simplify connections

Addition of LEDs and interface pads [power and SPI (serial peripheral interface) for testing and prototyping]

Removal of backup QFN pads to shrink die attach area



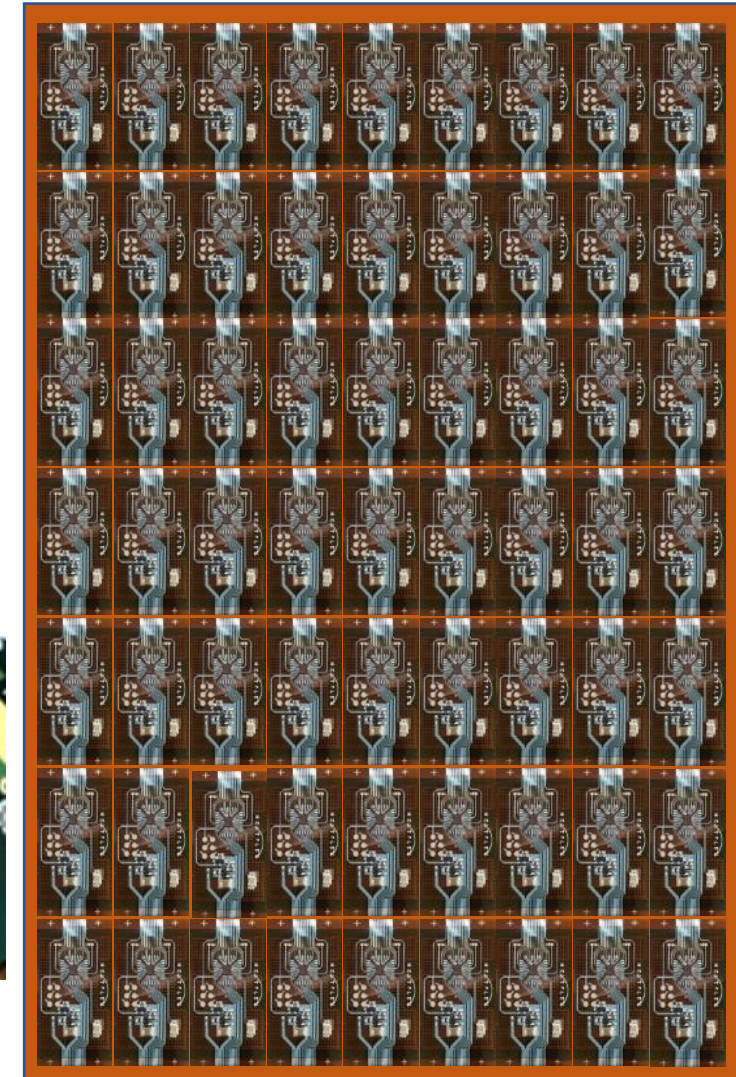
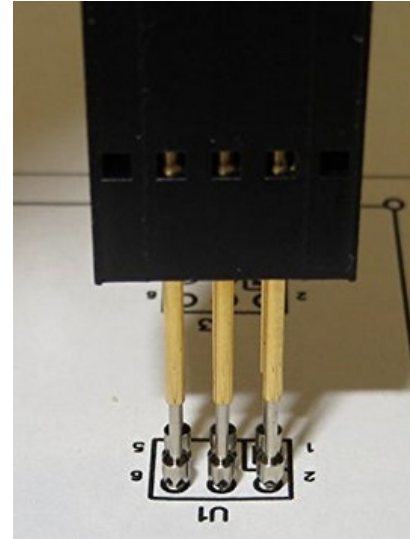
Standardization of passives to 0402 (4 mil x 2 mil US standard) size to maximize bend



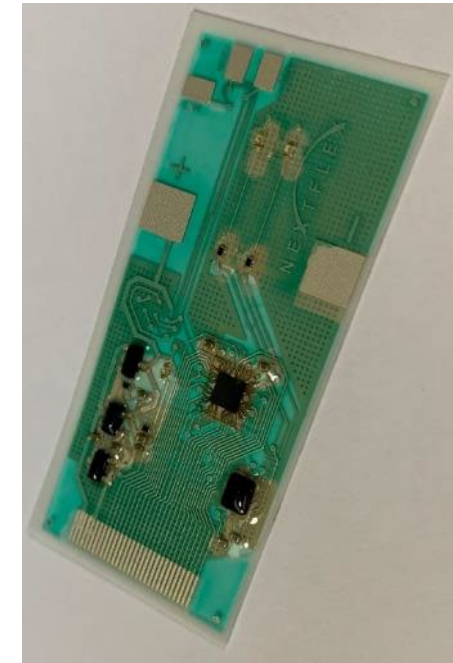
Packing of components into smaller area

Automated Programming

- Programming was automated using a FANUC® robot
- Robot would automatically detect, properly place pogo pin connector on pads, program and test each device on the sheet using computer vision



Singulation



- Up to 35 devices are produced on a single 8"x12" sheet
- Once fabrication of an entire sheet had been completed and the devices are tested, the devices are singulated and prepared for distribution

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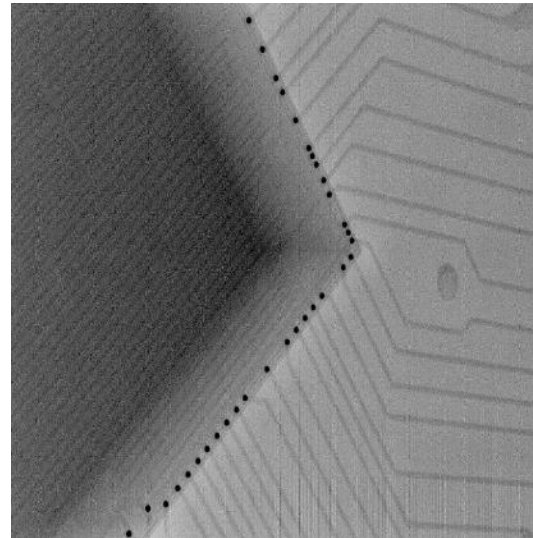
IMPROVEMENTS AND APPLICATIONS

Stud Bumping for Fine Pitch Integration

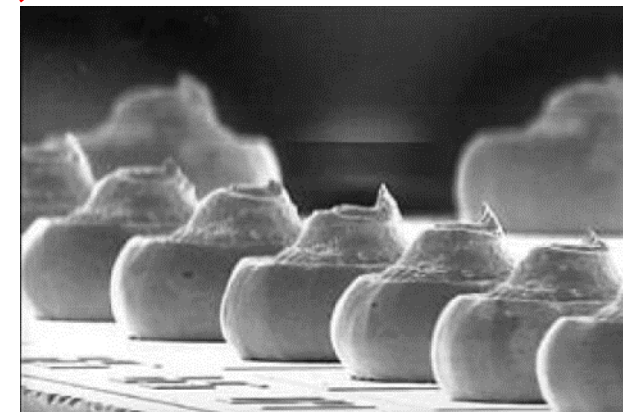
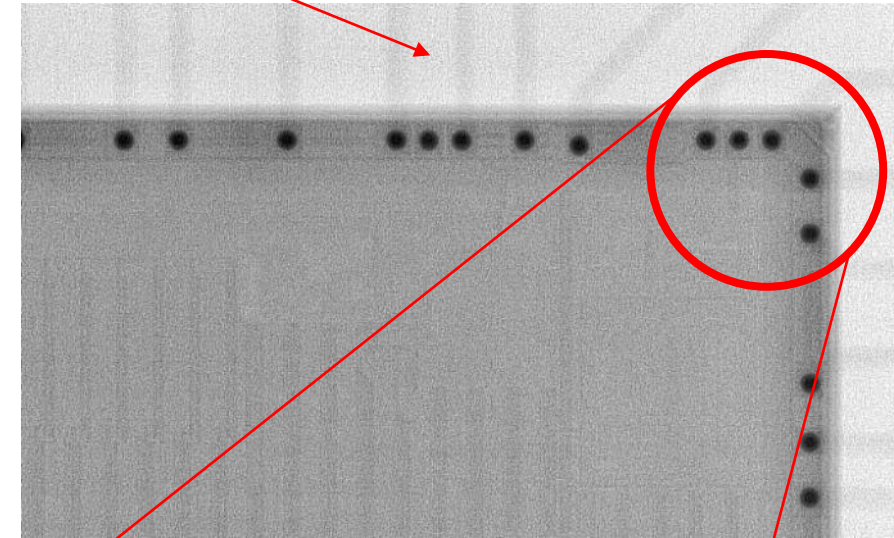
- Starting with unpackaged die
- Pads are inset below the passivation layer requiring bumps to allow for flip-chip attach
- Gold pads also remove issues with oxide layers on aluminum pads

Two Options

- ENIG (Electroless Nickel Immersion Gold)
 - Low Cost
 - Scalable
 - Limited pitch due to mushrooming
 - Limited control over where plating occur
- Stud Bumping
 - Higher Cost
 - Scalable
 - Ability to do fine pitch bumping (8 um gaps)
 - No plating in undesired areas
 - Spikes easily into silver traces

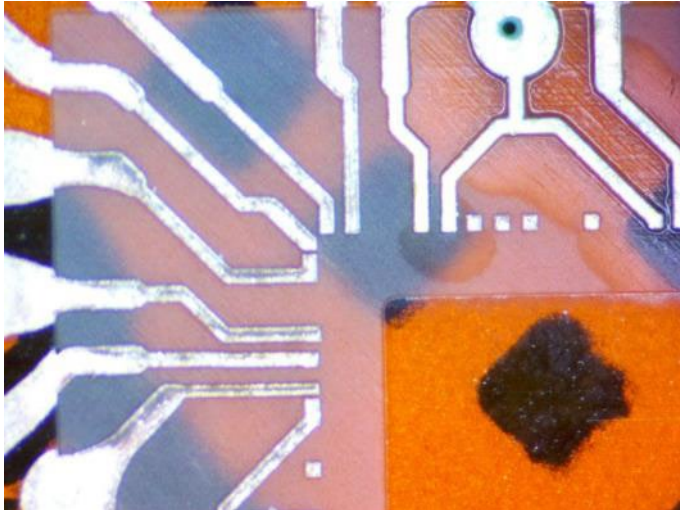


50um traces



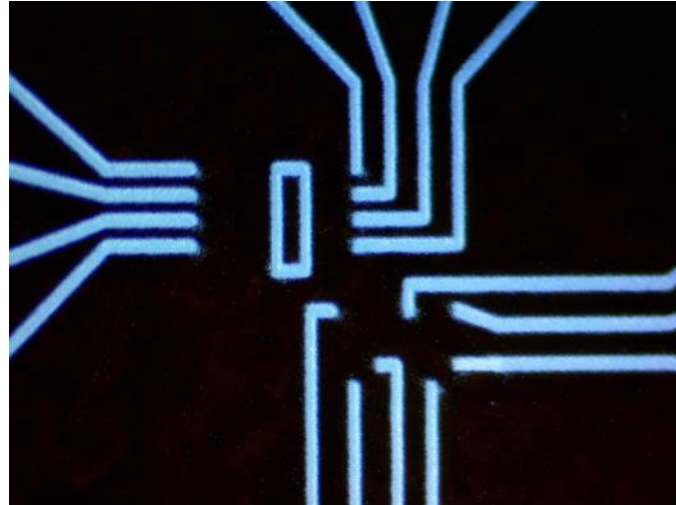
Die Interconnect Methods

Laser ablation



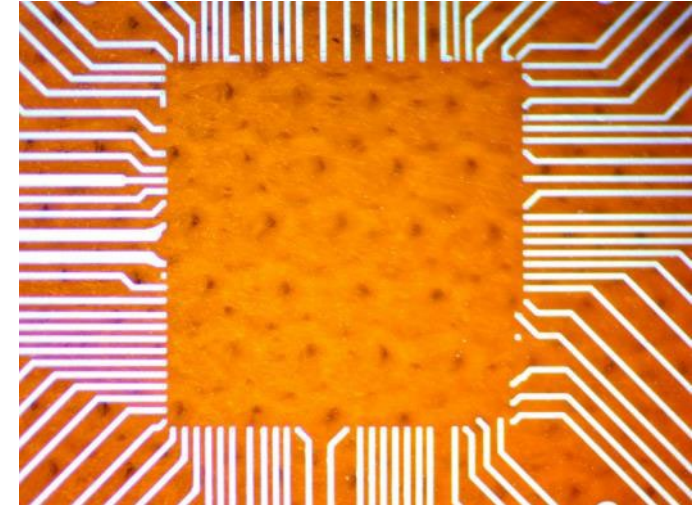
- **ESI 5200 femtosec green laser**
- Widest material set
- Thickest trace height
- Spot size ~10um

Aerosol jet



- **Optomec AJ5X**
- Great for prototyping
- Multi-axis printing
- Wide material set

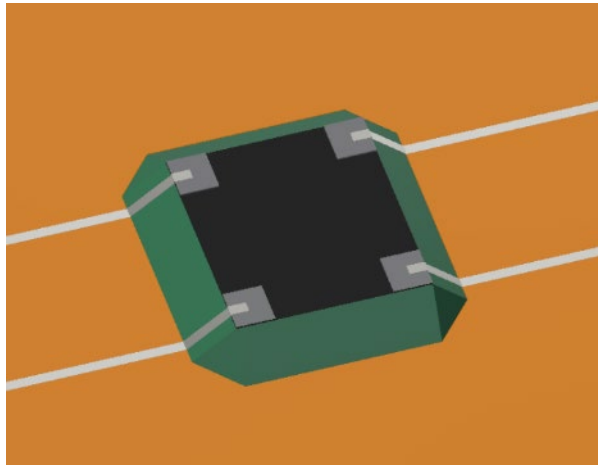
Gravure offset



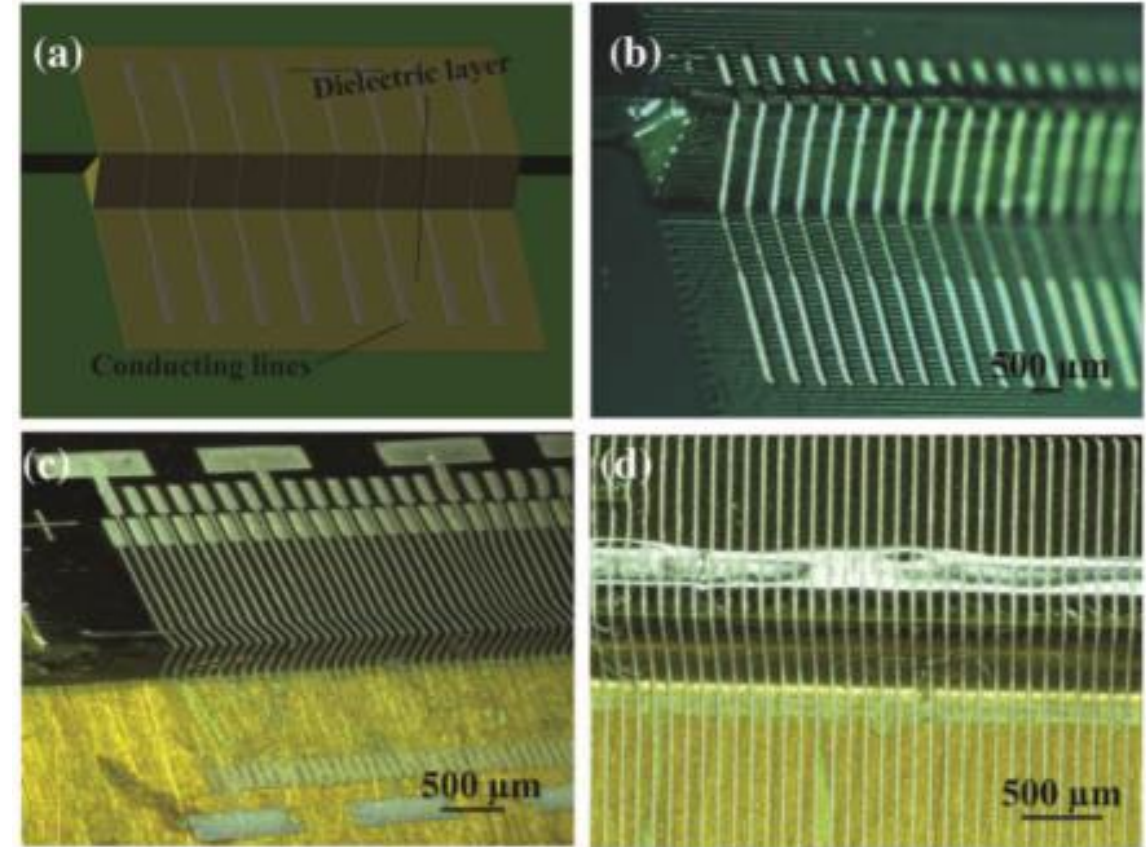
- **Komori sheet-fed**
- Fastest process by sheet
- Excellent edge definition
- Thin trace height ~2um

Pads-Up Die Attach

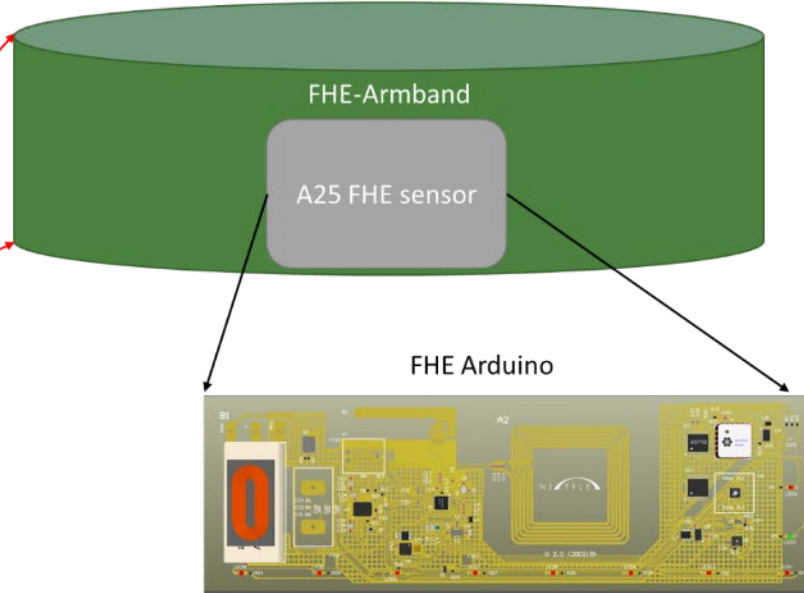
- Die attach alternate methods
 - Pads up & Print to pads
 - Print ramp for interconnect (aerosol jet or extrusion)



- Others
 - Flip chip w/ **magnetic field-oriented particles**
 - Stencil print bumps on substrate / attach with non-conductive underfill



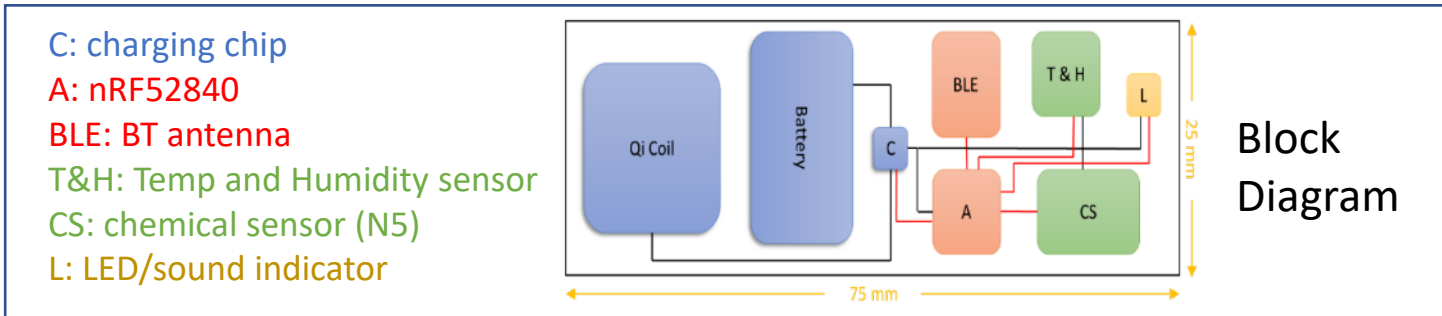
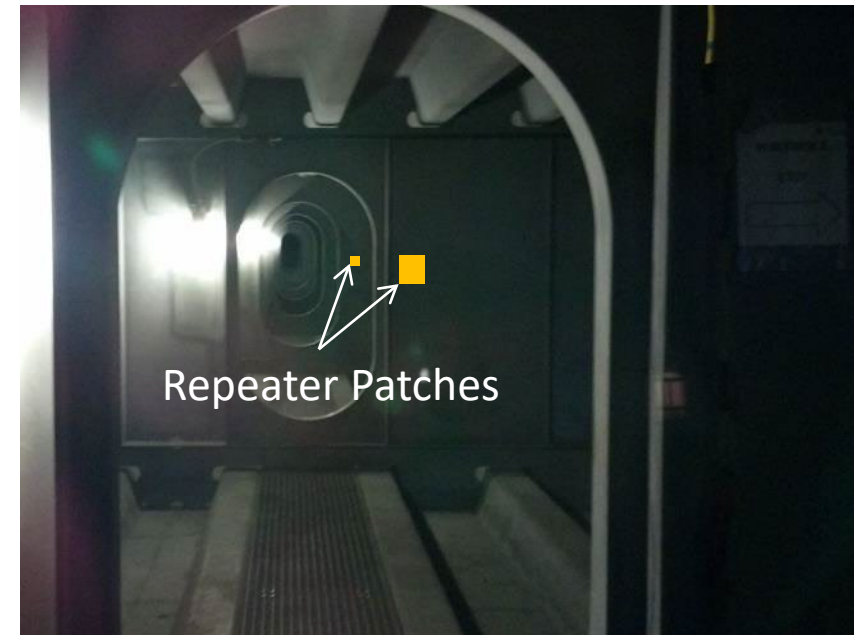
Wearable Chemical and Environmental Sensor



Confined Space Monitoring

Low cost repeaters can be placed at intervals to rebroadcast message to personnel outside the immediate area

Microcontrollers are capable of mesh networking

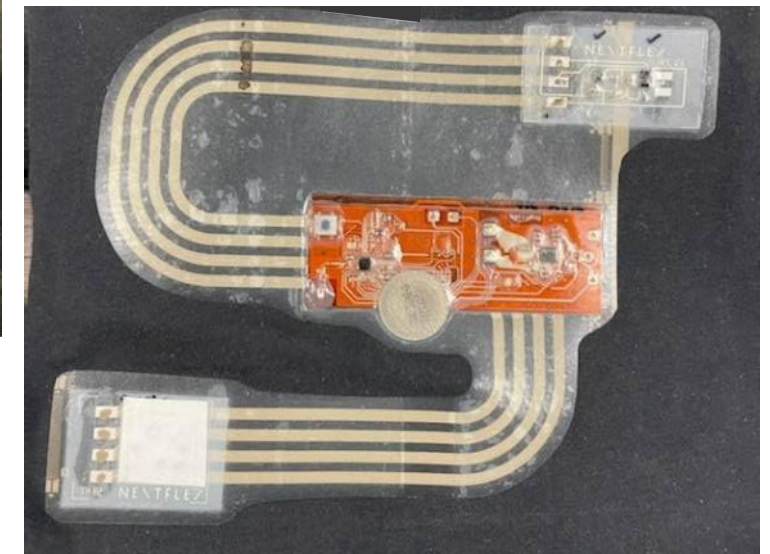


Sensing built into clothing can monitor for health and safety issues and send warnings when safety issues are detected



Multi-Domain Operations Wearable Sensor System

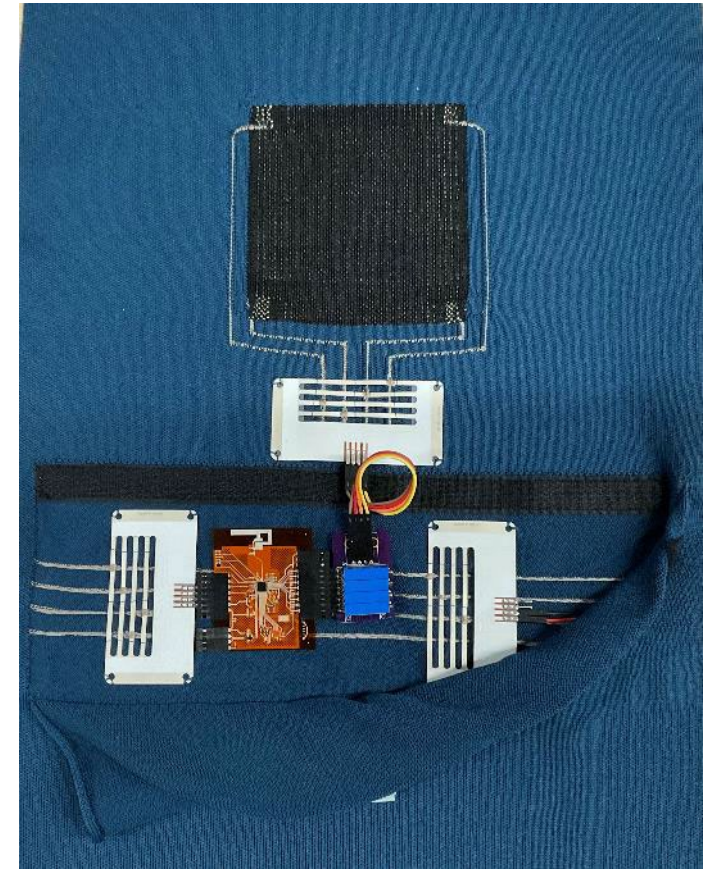
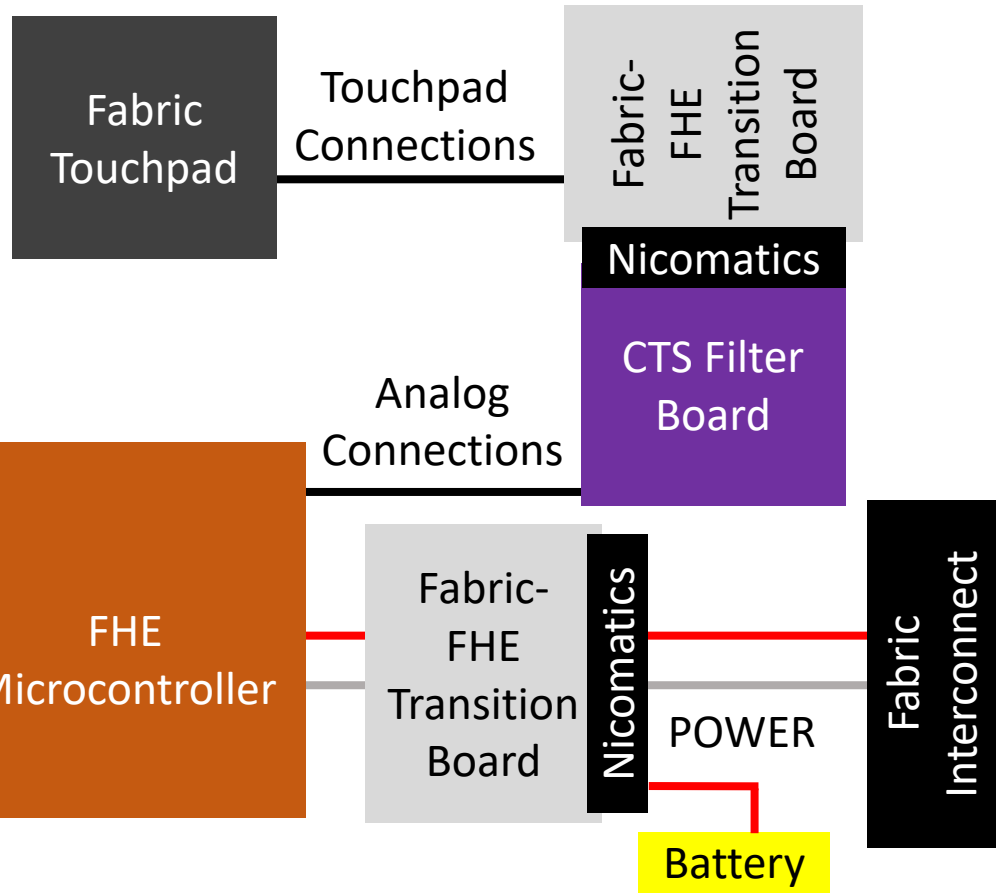
- Wearable Sensor System
- Unobtrusive monitoring of soldiers
- Provides real-time data
 - Vital signs
 - Stress
 - Heat/cold injuries
 - Local environmental data
 - Signs of fatigue
- Flexible electronics laminated onto a regular shirt
- RF communications interface with wearable/embedded sensors



Microcontroller with Textile Integration

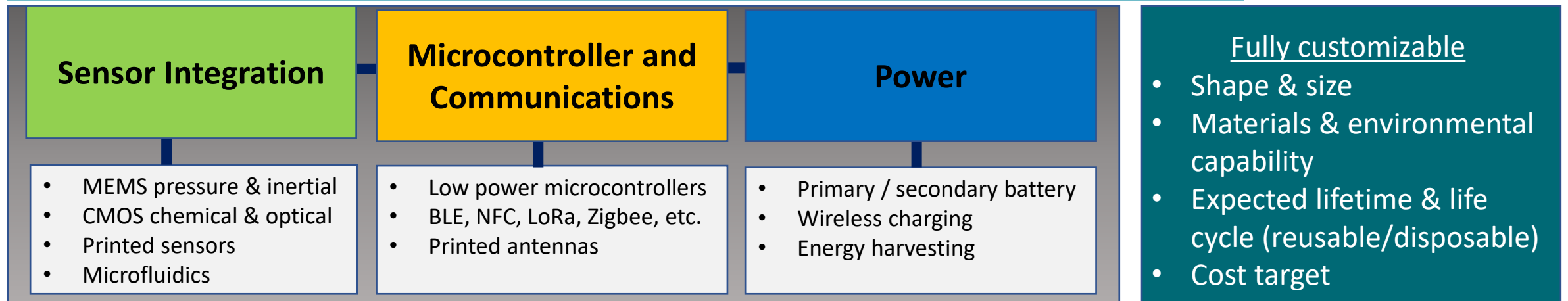
Capacitive Touch System (CTS) enables touch localization along a conductive fabric surface.

A microcontroller FHE board can be integrated into the CTS to develop a fully flexible solution.



Sensing built into clothing requires leads in fabric – conductive threads, switches, touch sensors – but processing will be in additive components. Interfaces are important. Are they rigid or flexible? Electrical? Electro-optic?

Summary – A US Electronics Manufacturing Opportunity



Flexible Hybrid Electronics enables unique products

- Wearables, medical devices, machinery monitoring, secure authentication, IOT sensor nodes
- Structurally integrated sensors
- Conformal antenna arrays on aircraft

Printing and additive processes allow for new form factors, high mix, digital manufacturing

The FHE toolkit enables tighter integration of circuits and novel packaging

- Bare die integration, eliminating discrettes, new design freedom
- Structural & system level integration

It takes a multidisciplinary effort to build FHE:

- Electronics, printing, robotics, material science, plastics processing, software

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